

CFG Orion

***Release Notes***

**Version: ORION-20.Q1**

**Revision: 0.0**

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CFG Orion 20.Q1 Release Notes

About This Document

This document lists the release notes for CFG Orion. Using CFG NocStudio, users can define NoC architectures, describe specifications and requirements, optimize the NoC design and finally generate the NoC IP files such as RTL, testbench, synthesis scripts, NoC IP documentation etc.

Audience

This document is intended for users of NocStudio:

* NoC Architects
* NoC Designers
* SoC Architects

Prerequisite

Before proceeding, you should generally understand:

* Basics of Network on Chip technology
* AMBA interconnect standards

Related Documents

The following documents can be used as a reference to this document.

* CFG NocStudio Orion User Manual
* CFG Orion IP Integration Spec

Customer Support

For technical support about this product and general information, contact CFG Support.

Revision History

|  |  |  |
| --- | --- | --- |
| Revision | Date | Updates |
| 0.0 | Jan 29, 2020 | Initial Version |

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# Deliverables

* CFG NocStudio Package contains N7 version of the tool supporting 16 layers and 256 bridges.
* NocStudio executable with interactive GUI.
* Verification checkers to be used in the DV environment.
* Sanity Test Bench.
* Documentation
  1. NocStudio User Manual: The User Guide describes how to set up a system using NocStudio and how to use it to generate CFG IP.
  2. IP Integration Spec: The Integration Manual describes how to integrate a configured network into a larger subsystem.
  3. Technical Reference Manual: The Technical Reference Manual describes how the functionality of the various NoC elements, the features and functions available, and how to dynamically change the functions using the programmer’s mode.

# Installation

## Licensing

NocStudio uses FlexLM based licensing hosted by Intel Central Licensing group using two dedicated license servers: one in Santa Clara and the other is located in Israel.

In addition to LM\_PROJECT, a linux environmental variable *NETSPD\_LICENSE\_FILE* shall be set as shown below in order to access the licenses. The LM\_PROJECT is essential for users not to check out the wrong combination of license features by accident.

setenv NETSPD\_LICENSE\_FILE [7010@netspeed01p.elic.intel.com:7010@netspeed02p.elic.intel.com](mailto:7010@netspeed01p.elic.intel.com:7010@netspeed02p.elic.intel.com)

For teams without LM\_PROJECT defined, a node-locked license file may be issued. Simply copy over the license file under NocStudio installation directory and renamed it as “license.dat”. If the license file resides in a separated folder, user may set environment variable *LM\_LICENSE\_FILE* before opening NocStudio.

## Deliverables / Tarball set

The CFG IPs and their configuration tool NocStudio have been packaged individually for maximum flexibility allowing mix and match. Each release is tagged with <yy><mm> where yy is the last 2 digits of the year and mm is the month in integer. As an example, release in Jan 2019 will be referenced as 1901 release. Un-tar all individual tarballs delivered as part of the tarball set using the command below.

linux% tar zxvf <tarball\_name>.tar.gz

Here is a snippet of tarball set in 1910 release: netspeed-<release>.<package>.tar.gz

**Tarball name Description Category**

netspeed-1910.tar.gz NocStudio Base  
netspeed-1910.iculibpkg.tar.gz Unicode ICU lib package Base

netspeed-1910.cruxpkg.tar.gz Crux IP package (non-AMBA) NSIP IP

netspeed-1910.orionpkg.tar.gz Orion IP package AMBA IP  
netspeed-1910.geminipkg.tar.gz Gemini IP package AMBA IP  
netspeed-1910.pegasuspkg.tar.gz Pegasus IP package AMBA IP

netspeed-1910.ocppkg.tar.gz OCP support package Connectivity  
netspeed-1910.daupkg.tar.gz Deadlock Avoidance Unit System

netspeed-1910.syscpkg.tar.gz SysC (PA) support package Flow

netspeed-1910.cpp61pkg.tar.gz C++ Modeling API support Flow  
package for gcc 6.1

**Note**:  
The release makes use of Qt libraries covered under LGPL: http://qt-project.org/downloads

# Feature Updates

# EDA Tool Compatibility

* Cadence EDA tools were used for verification and synthesis of this product.
* Xcelium 19.03-s003
* Design Compiler RTL Synthesis N-2017.09-SP3
* HAL Linting tool 15.20.027
* Conformal 16.20.s240
* Compatibility testing has been done with VCS N-2017.12-SP2-4.
* For Platform Architrect, used GCC version is gcc-6.1.0a. (Backward compatible upto gcc-5.2.0-64)
* Please refer to IP Integration specification to enable/disable specific CFG checker in order to resolve or workaround any verification related issues, if any.

Contact your CFG or Synopsys support team for assistance.

# Errata

None

# Changes to Commands and Properties

## Command Changes

|  |  |
| --- | --- |
| **Property Name** | **Comment** |
| add\_regbus\_controller | This command adds a Regbus Controller (RBC) with its RBM and optional Axi4s bridge to tunnel requests from masters in the NoC, or APBM| AXI4LM| IOSFSB| IOSFSB2APB interfaces to drive external requests into the RBC. |
| vifce\_prop | This command is used to set or view a named property of a particular Interface's virtual interface and an optional direction in/out, w.r.t the host. |
| reset\_mesh | This command resets the previously created mesh. |

## Default Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| rssb\_router | no | Deprecated. By default all routers are RSSB. |

## Mesh Property Changes

None

## Bridge Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| axi4m\_ar\_max\_beats | 256 | This property is used to set the limit on the burst length for reads. |
| axi4m\_aw\_max\_beats | 256 | This property is used to set the limit on the burst length for writes. |
| axi4s\_memory\_type | device | This property indicates the Memory Type fot the AXI slave as described in the AXI spec. |
| cmi\_rd\_req\_max\_credit\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| cmi\_rdcpl\_max\_credit\_error\_fatal | no | This property is used to control whether the interrupt raise on read credit overflow will be fatal or non-fatal. |
| cmi\_route\_lookup\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| cmi\_rsp\_max\_credit\_error\_fatal | no | This property is control whether the interrupt raise on read credit overflow will be fatal or non-fatal. |
| cmi\_unsupported\_opcode\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |

## Host Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| llc\_data\_ram\_latency | 2 | This property specifies the latency of the data array for this cache. |
| llc\_tag\_ram\_latency | 2 | This property specifies the latency of the tag array for this cache. |

## Interface Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| cmi\_supports\_interleaved\_response | no | For CMIM bridges, indicates whether the CMI VC can accept interleaved read data completion responses. |

## Link Property Changes

None

## Router Property Changes

None

## VC Property Changes

None

## CSB Storage Property Changes

None

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